

an analog/digital converter electrically connected to said output port of said phase comparator, said analog/digital converter having an output end;

a memory electrically connected to said output end of said analog/digital converter;

a first switch positioned between said first input port of said phase comparator and said clock pad;

a second switch positioned between said crystal oscillator and said clock pad, wherein a stream direction of said second switch is in reverse to a stream direction of said first switch; and

a logic control device for controlling said first switch and said second switch.

15.(new) The self-calibratable oscillating device of Claim 14, further comprising:

a power pad; and

a high voltage detector electrically connected to said power pad and said logic control device.

16. (new) The self-calibratable oscillating device of Claim 14, further comprising:

an embedded clock generator suitable for providing an operation clock for said logic control device.

17. (new) The self-calibratable oscillating device of Claim 16, wherein said embedded clock generator is a resistance-capacitor oscillator.

18. (new) An ASIC for a crystal oscillator comprising:

a system bus;

an embedded CPU electrically connected to said system bus;

a system memory electrically connected to said system bus;
a clock pad suitable for receiving a reference clock;
a phase comparing means for generating a phase difference signal between said reference clock and a clock of an oscillating device of the crystal oscillator;
an analog/digital converting means for converting the phase difference signal into a digital signal;
a first switch positioned between said phase comparing means and said clock pad; and
a second switch positioned between the oscillating device and said clock pad.

19. (new) The ASIC for a crystal oscillator of Claim 18, further comprising:

a registering means electrically connected to said system bus, said registering means for storing data of said system memory when said second switch is turned on.